

IN THE DRAWINGS

Please approve the changes to the drawings as outlined in the attached Letter to the Draftsperson, and as shown in the accompanying revised replacement drawings. Specifically, Figure 13 is being labeled as "PRIOR ART".

REMARKS

The above amendments to the above-captioned application along with the following remarks are being submitted as a full and complete response to the Office Action dated June 28, 2005. In view of the above amendments and the following remarks, the Examiner is respectfully requested to give due reconsideration to this application, to indicate the allowability of the claims, and to pass this case to issue.

Status of the Claims

As outlined above, claim 2 is being canceled without prejudice or disclaimer, while claim 1 is being amended to correct formal errors and to more particularly point out and distinctly claim the subject invention. Specifically, claim 1 is being amended to substantially incorporate the recitation of claim 2. Support for the amendments and the invention as a whole may be found throughout the specification and as will be discussed further hereinbelow.

Additional Amendments

The drawings, specifically Figure 13, are being amended to correct formal errors and to better disclose and describe the features of the present invention as claimed. Applicant hereby submits that no new matter is being introduced into the application through the submission of this response.

Formal Objections or Rejections

The Examiner objected to the drawings as Figure 13 should be labeled as "Prior Art". As noted above and in the accompanying Letter to Draftsperson, Figure 13 is being labeled as "PRIOR ART."

Prior Art Rejections

The Examiner rejected claims 1-9 under 35 U.S.C. § 102(e) as being anticipated by Awakura et al. (2005/0068270). Applicants have reviewed the above rejection, and hereby respectfully traverse.

The present invention as recited in claim 1 is directed to an image display device comprising: an image display portion in which a plurality of pixels are arranged in a matrix; a plurality of signal lines wired in said image display portion to carry a voltage signal to said

pixels; and a drive circuit to control voltage on each said signal line. Each pixel comprises a light emitting element and a pixel circuit which controls the intensity of light emission of said light emitting element. The image display device is equipped with a pixel circuit voltage detecting circuit for placing the pixel circuit included in each said pixel in at least one of a disconnection state from said signal line, a connection state to said signal line, and resistive connection state wherein said pixel circuit connects to said signal line with a sufficiently higher value of resistance than in said connection state. Finally, the image display device is equipped with a voltage addition means to add the voltage on said signal line and a signal voltage corresponding to image data to be displayed and output a sum voltage to said signal line again.

Figure 1 illustrates one embodiment of the pixel circuit voltage detecting circuit of the present invention. As shown, the circuit is illustrated as being configured by the TFT11, TFT12 and the resistor 17, as described on page 9, line 18 to page 10, line 12 in the specification. Also, the operation and effect of the pixel circuit voltage detecting circuit of the present invention are described on page 13, line 16 to page 19, line 7 of the specification.

Among the main features of the present invention, the pixel circuit voltage detecting circuit can place three states between the pixel circuit and the signal line: (1) a disconnection state, (2) a connection state, and (3) a resistive connection state. A concrete configuration of the pixel circuit voltage detecting means is shown in Figure 2, wherein the circuit is formed by TFT11, TFT12, and the resistor 17. In addition, according to Figure 4, TFT11 and TFT12 are driven in response to the waveform 4b and 4c. Therefore, the following three states are possible:

- (1) The pixel circuit voltage detecting circuit goes into a disconnection state, when 4b=Low, and 4c=Low. Specifically, the signal line 3 and the pixel circuit 2 are disconnected, and thus are in a disconnection state.
- (2) The pixel-circuit voltage detecting circuit goes into a connection state, when 4b=High, and 4c=Low. The signal line 3 and the pixel circuit 2 are connected through the TFT11, and thus are in a connection state.
- (3) The pixel circuit voltage detecting circuit goes into a resistive connection state, when 4b=Low, and 4c=High. The signal line 3 and the pixel circuit 2 are connected through the resistor 17 and TFT12, and thus are in a resistive connection state.

In contrast, the pixel circuit 141 in Fig. 2 of Awakura '270 has only two states. This is done via a connection or a disconnection between the signal line 111 and the pixel circuit 141 by the ON/OFF condition of the transistor 21.

The resistor 30 in Fig. 23 of Awakura '270 is merely a resistor of the power source line in order to connect between the display unit and the external power source line. The resistor is not to connect between the signal line and the pixel circuit. The resistor 30 exists in each of power source lines, but not in each of the pixel circuits. In addition, the resistor 30 cannot be controllably synchronized with the transistor 21 of Fig. 2.

Consequently, the structure and operation of Awakura '270 are both very different from those of the present invention as recited in claims 1 and 2 - 9. Awakura '270 cannot anticipate or render obvious each and every feature of the present invention as claimed.

Allowable Subject Matter

Applicants appreciate the Examiner's consideration in holding claims 10-17 allowable.

Conclusion

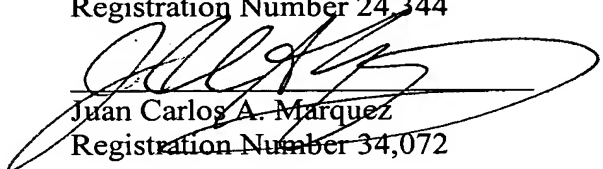
In view of all the above, Applicant respectfully submits that certain clear and distinct differences as discussed exist between the present invention as now claimed and the prior art references upon which the rejections in the Office Action rely. These differences are more than sufficient that the present invention as now claimed would not have been anticipated nor rendered obvious given the prior art. Rather, the present invention as a whole is distinguishable, and thereby allowable over the prior art.

Favorable reconsideration of this application as amended is respectfully solicited. Should there be any outstanding issues requiring discussion that would further the

prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicant's undersigned representative at the address and phone number indicated below.

Respectfully submitted,

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